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Calafut et al.(10) **Pub. No.: US 2013/0075808 A1**(43) **Pub. Date: Mar. 28, 2013**(54) **TRENCH MOSFET WITH INTEGRATED
SCHOTTKY BARRIER DIODE**(52) **U.S. Cl.**USPC **257/328**; 257/476; 257/E29.338;
257/E27.016(75) Inventors: **Daniel Calafut**, San Jose, CA (US); **Yi Su**, Cupertino, CA (US); **Jongoh Kim**, Cupertino, CA (US); **Hong Chang**, Saratoga, CA (US); **Hamza Yilmaz**, Saratoga, CA (US); **Daniel S. Ng**, Campbell, CA (US)

(57)

ABSTRACT

A Schottky diode includes a semiconductor layer formed on a semiconductor substrate; first and second trenches formed in the semiconductor layer where the first and second trenches are lined with a thin dielectric layer and being filled partially with a trench conductor layer and remaining portions of the first and second trenches are filled with a first dielectric layer; and a Schottky metal layer formed on a top surface of the semiconductor layer between the first trench and the second trench. The Schottky diode is formed with the Schottky metal layer as the anode and the semiconductor layer between the first and second trenches as the cathode. The trench conductor layer in each of the first and second trenches is electrically connected to the anode of the Schottky diode. In one embodiment, the Schottky diode is formed integrated with a trench field effect transistor on the same semiconductor substrate.

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